

Please type a plus sign (+) inside this box → ☐

PTO/SB/05 (08-00) (modified)

Approved for use through 9/30/2001, OMB 0651-0032

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

11/28/00

JC957 U.S. PTO

# UTILITY PATENT APPLICATION TRANSMITTAL

(only for new nonprovisional applications under

37 CFR 1.53(b))

Attorney Docket Number 4616 US

First Named Inventor Bakker

Title Power Assisted Automatic Supervised Classifier Creation Tool For Semiconductor Defects

Express Mail Label No. EL566199679US

JC639 U.S. PTO  
09/22/00

11/28/00

## APPLICATION ELEMENTS

- ☒ Fee Transmittal Form (in duplicate)
- ☐ Applicant claims small entity status.  
See 37 CFR 1.27
- ☒ Specification *Total Pages*   
(preferred arrangement set forth below)
  - ☐ Descriptive Title of the Invention
  - ☐ Cross Reference(s) to Related Case(s)
  - ☐ Statement Regarding Fed sponsored R & D
  - ☐ Background of the Invention
  - ☐ Brief Summary of the Invention
  - ☐ Brief Description of the Drawing(s)
  - ☐ Detailed Description
  - ☐ Claim or Claims
  - ☐ Abstract of the Disclosure
- ☒ Drawing(s) (35 U.S.C. 113) *Total Sheets*
- Oath or Declaration
  - ☒ New Declaration *Total Pages*   
☐ Executed (original or copy)
  - ☐ Copy from a prior application (37 CFR 1.63(d))  
(for continuation/divisional with Box 17 completed)
    - ☐ DELETION OF INVENTOR(S)  
Signed statement attached deleting inventor(s)  
named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).
- ☐ Application Data Sheet. See 37 CFR 1.76

## ACCOMPANYING APPLICATION PARTS

- ☐ Assignment Papers (cover sheet & document(s))
- ☐ Certified Copy of Priority Document(s) (if foreign priority is claimed)
- ☒ Information Disclosure Statement & PTO-1449  
☐ Copies of IDS Citation(s)
- ☐ Preliminary Amendment
- ☒ Return Postcard
- ☐
- ☐
- ☐
- ☐
- ☐

### ADDRESS TO:

Box Patent Application  
Commissioner for Patents  
Washington, D.C. 20231

17. If a **CONTINUING APPLICATION**, check appropriate box and supply the requisite information below and in a preliminary amendment or in an Application Data Sheet under 37 CFR 1.76:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No: \_\_\_\_/\_\_\_\_

Prior application information: Examiner: \_\_\_\_\_ Group/Art Unit: \_\_\_\_\_

**For CONTINUING OR DIVISIONAL APPS only:** The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 5b, is considered a part of the disclosure of the accompanying continuing or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

## 18. CORRESPONDENCE ADDRESS

☐ Customer Number or Bar Code Label *(Insert Customer No. or Attach bar code label here)* Or ☐ Correspondence address below

NAME	Laura A. Majerus, Fenwick & West LLP				
ADDRESS	Two Palo Alto Square				
CITY	Palo Alto	STATE	CA	ZIP CODE	94306
COUNTRY	U.S.A.	TELEPHONE	(650) 858-7152	FAX	(650) 494-1417
Name (Print/Type)	Laura A. Majerus			Registration No. (Attorney/Agent)	33,417
Signature	Laura Maji			Date	November 28, 2000

0002/PTO(modified)  
Rev. 10/2000

U.S. Department of Commerce  
Patent and Trademark Office

## FEE TRANSMITTAL

### TOTAL AMOUNT OF PAYMENT

Subtotal (1) + Subtotal (2) + Subtotal (3) = **(\$1150.00)**

### Complete if Known

Application Number	To Be Assigned
Filing Date	Herewith
First Named Inventor	Bakker
Group Art Unit	To Be Assigned
Examiner Name	To Be Assigned
Attorney Docket Number	4616 US

### METHOD OF PAYMENT

#### 1. The Commissioner is hereby authorized to:

- ☐ Charge the indicated fees to the below mentioned deposit account.
- ☐ Charge any additional fee required under 37 CFR 1.16 - 1.21 or credit any over payments to the below mentioned deposit account. <sup>†</sup>
- ☐ Applicant claims small entity status See 37 CFR 1.27

Deposit Account Number:  
Deposit Account Name:

☒ Duplicate Copy of this authorization is attached

#### 2. ☐ Payment Enclosed:

☐ Check ☐ Credit Card ☐ Other

### FEE CALCULATION (continued)

#### 3. ADDITIONAL FEES

Large Entity Fee Code/Fee	Small Entity Fee Code/Fee	Fee Description	Fee Due
105/\$130	205/\$65	Surcharge - late filing fee or oath	
127/\$50	227/\$25	Surcharge-late provisional filing fee or cover sheet	
147/\$2,520	147/\$2,520	For filing a request for reexamination	
115/\$110	215/\$55	Extension for response within first month <sup>†</sup>	
116/\$390	216/\$195	Extension for response within second month <sup>†</sup>	
117/\$890	217/\$445	Extension for response within third month <sup>†</sup>	
118/\$1,390	218/\$695	Extension for response within fourth month <sup>†</sup>	
128/\$1,890	228/\$945	Extension for response within fifth month <sup>†</sup>	
119/\$310	219/\$155	Notice of Appeal	
141/\$1,240	241/\$620	Petition to revive unintentionally abandoned application	
142/\$1,240	242/\$620	Utility Issue Fee (Or Reissue)	
143/\$440	243/\$220	Design Issue Fee	
122/\$130	122/\$130	Petitions to the Commissioner	
126/\$180	126/\$180	Submission of Information Disclosure Statement	
179/\$710	279/\$355	Request for Continued Examination (RCE)	
581/\$40	581/\$40	Recording each patent assignment per property (times number of properties)	
146/\$710	246/\$355	Filing a submission after final rejection (37 CFR 1.129(a))	
149/\$710	249/\$355	For each additional invention to be examined (37 CFR 1.129(b))	
Other fee (specify):			
Other fee (specify):			
<b>SUBTOTAL (3)</b>			<b>(\$ 0.00)</b>

### FEE CALCULATION (fees effective 10/01/2000)

#### 1. FILING FEE

Large Entity Fee Code/Fee	Small Entity Fee Code/Fee	Fee Description	Fee Due
101/\$710	201/\$355	Utility Filing	<b>710</b>
106/\$320	206/\$160	Design Filing	
108/\$710	208/\$355	Reissue	
114/\$150	214/\$75	Provisional Filing	
<b>SUBTOTAL (1)</b>			<b>(\$ 710.00)</b>

#### 2. CLAIMS

Large Entity Fee Code/Fee	Small Entity Fee Code/Fee	Fee Description
103/\$18	203/\$9	Claims in excess of 20
102/\$80	202/\$40	Independent claims in excess of 3
104/\$270	204/\$135	Multiple dependent claim
109/\$80	209/\$40	Reissue independent claims over original patent
110/\$18	210/\$9	Reissue claims in excess of 20 and over original patent

(Col. 1)		(Col. 2)		(Col. 3)		Fee		Fee Due	
For	No. of Existing Claims	minus*	Highest No. Previously Paid For	=	Extra**	x		=	
TOTAL	40	minus*	20 or 0	=	20	x	18	=	360
INDEP	4	minus*	3 or 0	=	1	x	80	=	80
[ ] First presentation of multiple dependent claim									0

\* Subtract the greater number of Col. 2

\*\* If the difference between Col. 1 and Col. 2 is less than zero, then enter "0" in Col. 3

**SUBTOTAL (2)** **\$ 440.00**

#### SUBMITTED BY

Typed or Printed Name **Laura A. Majerus**

Signature *Laura Majerus*

#### Complete (if applicable)

Reg. Number **33,417**

Date **November 28 2000**

<sup>†</sup> Request for Extension of Time per 37 CFR 1.136 (a)(3) made hereby  
Rev. 09/30/2000

**U.S. Patent Application**

for

**POWER ASSISTED AUTOMATIC SUPERVISED CLASSIFIER  
CREATION TOOL FOR SEMICONDUCTOR DEFECTS**

Inventors:

Dave Bakker, Saibal Banerjee, and Ian Smith

Fenwick & West LLP  
2 Palo Alto Square  
Palo Alto, CA 94306  
650-494-0600

Atty Docket No. 22120-04616  
Express Mail No. EL566199679US

**POWER ASSISTED AUTOMATIC SUPERVISED CLASSIFIER  
CREATION TOOL FOR SEMICONDUCTOR DEFECTS**

Related Applications

5           This application claims priority under 35 U.S.C. § 119(e) to U.S. Provisional Application Serial No. 60/167,955 entitled "Power Assisted Automatic Supervised Classifier Creation Tool for Semiconductor Defects," of Bakker, Banerjee, and Smith et al., filed November 29, 1999.

10           The following applications are related to this application and are herein incorporated by reference:

1. U.S. Application Serial No. 08/958,288 of Hardikar et al., filed October 27, 1997.

2. U.S. Application Serial No. 08/958,780 of Hardikar et al., filed October 27, 1997.

15           The following U.S. patent is related to this application and is herein incorporated by reference:

1. U.S. Patent No. 5,226,118 to Baker et al., issued July 6, 1993.

**Background of the Invention**

Field of the Invention

20           The present invention relates generally to software programs and, more generally, to software-controlled optical, ebeam, or other types of inspection systems for semiconductor wafers.

Description of Background Art

25           As new materials, methods, and processes are introduced into semiconductor manufacturing, new defects are emerging in the manufacturing process that can greatly impact yield. These changes require chipmakers to adopt new technologies to detect and classify these yield-limiting defects more quickly, accurately and consistently in order to tighten their manufacturing processes and accelerate their yield-learning curve. At the

same time, shrinking product lifecycles and accelerated time-to-market requirements are forcing fabrication plants to speed their product ramp ups for new products to meet their profitability objectives. This, in turn, is driving the need for faster automatic defect classification (ADC) setup to ensure fabrication plants can reap the benefits of ADC  
5 without slowing the ramp process.

Currently existing optical inspection systems with automatic defect classification require human beings to visually inspect semiconductor wafers for suspected defects and to classify the types and causes of the defects in order to set up the automatic classification system. To perform this classification, a human being must sort through  
10 hundreds of images that are presented in random order. This process takes many hours and increases the cost of production. Moreover, even skilled human operators are somewhat slow and prone to error.

#### SUMMARY OF THE INVENTION

15 The described embodiments of the present invention receive images of defects and aid a user in classifying the types of defects represented. A graphical user interface allows a human user to manually classify the defect images via Automatic Supervised Classifier software and further allows the user to contrast his manual classifications with the classifications determined by Automatic Supervised Classifier software. In order to  
20 create an Automatic Supervised Classifier for semiconductor defect classification, the human user has to perform two manual tasks:

(i) Creation of a good Classification Scheme (which images the user will place into which classes).

(ii) Creation of a good training set of examples for the Automatic Supervised  
25 Classifier with this Classification Scheme.

The described embodiments of the invention provide a tool to help the human user achieve both these objectives in record time.

The embodiments described herein contain four main components that seamlessly interact with one another:

(i) Image Gallery: This is a graphical interface to display a list of images in an organized fashion.

(ii) Dynamic Automatic Supervised Classifier: Given a list of defects, the user is allowed to manually classify or train any set of defects. The rest of the defects are dynamically classified accordingly and the overall performance of the resulting classifier is calculated.

(iii) Dynamic Classifier Controls and Performance Tools: This allows the user to visualize and optimize the parameters of the classifier even further. As in (ii), the resulting performance is immediately visible.

(iv) Unsupervised Automatic Classifier (Natural Grouping): This groups the images in a way that allows the user to visualize the layout and structure of the feature space in terms of defect images, and assists the user in creating both a good classification scheme and a good training set of examples.

#### **BRIEF DESCRIPTION OF THE DRAWINGS**

Fig. 1(a) is a block diagram showing an overview of a semiconductor optical, ebeam, or other types of inspection systems.

Fig. 1(b) is a block diagram showing an overview of a semiconductor optical, ebeam, or other types of inspection systems using the present invention for inspection set-up.

Fig. 2 is a block diagram showing the interaction of a human user with sections of an embodiment of defect classifier software.

Fig. 3 shows an interface generated by defect classifier software in accordance with a preferred embodiment of the present invention.

Fig. 4(a) shows an example of a confusion matrix in the user interface where the manual and automatic classification are in agreement.

Fig. 4(b) shows an example of the confusion matrix in the user interface where the manual and automatic classification are not in agreement.

Figs. 5(a) and 5(b) shows examples of an interface that allows the user to display in sorted order the images in the working set and in the training set.

Fig. 6(a) is a flow chart showing a method for natural grouping of images in the working set.

Fig. 6(b) shows images organized and displayed in their natural groups.

Fig. 7 shows an expanded view of a training area in the user interface.

Fig. 8 shows an example user interface used in a preferred embodiment of the present invention.

Fig. 9 shows an additional user interface for the Classifier Function.

Figs. 10(a), 10(b), and 10(c) are block diagrams of systems in accordance with the present invention distributed over a network, such as the internet or an intranet.

#### **DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS**

The described embodiments of the present invention aid a user in classifying defect images input to the system from an outside source. For example, the images can be received from a scanning electron microscope (SEM), a defect inspection system, such as KLA-Tencor's 2132 inspection tool, or any similar instrument used to generate images and detect errors therein. In certain embodiments, the images can come from more than one type of input source. For example, images may be received from both an SEM and a 2132 inspection tool. Different types of images received from different sources can aid in defect classification, since different types of images produce more information is provided concerning the defects to be classified. The images also can be point locations on wafers.

Fig. 1(a) is a block diagram showing an overview of a semiconductor optical, ebeam, or other types of inspection systems. As discussed above, defect images are preferably received from an outside source, such as an optical, ebeam, or other types of inspection systems 102, an SEM or a 2132 inspection tool. The defect images tell a classification system 104 that defects have occurred, but does not classify the defects as to types or causes. After the defects are classified, as described below, they are sent to an analyzer 106, such as KLA-Tencor's KLArity system, an example of which is

described in the above-referenced U.S. Application Serial Nos. 08/958,288 and 08/958,780.

Fig. 1(b) is a block diagram showing an overview of a semiconductor optical, ebeam, or other types of inspection systems using the present invention for inspection set-up. As discussed above, defect images are preferably received from an outside source, such as an optical, ebeam, or other types of inspection systems 102, an SEM or a 2132 inspection tool. These images can relate to etch, photolithography, deposition, CMP, or to some other manufacturing process. The defect images tell a classification system 104 that defects have occurred, but does not classify the defects as to types or causes. After the defects are classified, as described below, they are sent to an analyzer 106, such as KLA-Tencor's KLAry system, an example of which is described in the above-referenced U.S. Application Serial Nos. 08/958,288 and 08/958,780. The output of the analyzer 106 is used as feedback to fine tune the inspection system.

For example, depending on the number of errors found and the accuracy desired, the inspection system may be fine tuned to raise or lower the sensitivity of the system. For example, if the system is finding too many errors or errors that are not relevant to the particular manufacturing process, the system may be fine tuned to lower its sensitivity, resulting in fewer errors detected. As another example, if not enough errors are being detected or if errors of a certain type are not being detected, the inspection system may be adjusted to become more sensitive, so as to detect data that will result in the detection of more errors or of errors of a desired type. In certain embodiments, it has been observed that as sensitivity is increased, error detection increases exponentially. In such a system, feedback to the ADC can be used to control inspection parameters including but not limited to: illumination, sensitivity or sensing (optical, ebeam, etc), threshold of detection, filtering, and/or polarization.

In another embodiment, the feedback is used to control the manufacturing process. For example, feedback from the analysis portion could be used to shutdown the process or a certain machine if too many errors are detected from that machine. Similarly, the feedback could be used to re-route lots to machines or processes with the lowest errors rates on either a static or a dynamic basis.



In another embodiment, the inspection and analysis/classification process is performed in real-time during the inspection process instead of as a separate process. (An example of this is shown in the system of Fig. 10(c)). In such a system, inspection system 1052 is shown inside the system 1004 to indicate that it is part of the same system as the classifier 1056.

Fig. 2 is a block diagram showing the interaction of a human user with an embodiment of defect classifier software. A working set 208 of images, such a wafer defect images, is displayed for user review (as discussed below in connection with Fig. 3). A human user 210 can review the defect images in the working set.

The human user can also request that the images be organized by natural grouping 212 and displayed according to this organization. The human can manually classify the defect images into classes (also called "bins") according to the human's understanding of the type of defect represented by the image. Currently, the extracted features of the defect images are used to naturally group the defect images, using a Kohonen mapping technique. Kohonen mapping is described, for example in T. Kohonen, "The Self-Organizing Map," Proceedings of the IEEE, Vol. 78, 1990, pp. 1464-1480, which is herein incorporated by reference. Other methods can be used for natural grouping, such as a K-means; the method described in N. Otsu, "A Threshold Selection Method from Gray-Level Histograms," IEEE Trans. Systems, Man, and Cybernetics, Vol. SMC-9, 1979, pp.62-66 (which is herein incorporated by reference); or any other appropriate technique or method that groups defect images according to common features. In a described embodiment, both the natural grouping 212 and the automatic classifier 204 use the same feature set.

In addition, the human user can select images from the working set to be placed in a "training set" of images. The user then manually adds images/defects to the class/bins of the training set. Features are extracted from the selected images and stored along with the class/bin during a "train classifier" operation. The classifier then classifies a set of images (such as the set W-T) and the user reviews the errors found in the classifier's decisions. For example, the user may view the confusion matrix to determine where the classifier differed from the user's classifications. The user then

improves the training set by adding deleting, or reclassifying images via, e.g., a drag and drop interface and reassesses the classifier's performance until a satisfactory result is achieved.

The images in the training set are sent to feature extractor software 206, which  
5 extracts a set of predefined features for each image in the training set. The data structures storing a set of features for an image is called the image's "feature vector." A feature vector contains the values for each feature for a particular image.

The predefined features preferably extracted from the training set include, but are not limited to:

10 a) features extracted from an image, such as: size, brightness, color, shape, texture, moment of inertia, context, proximity to wafer features or other defects, connectivity to adjacent features or other defects, other yield relevant properties derived from the image (e.g. short, open, bridging, particles, scratches, etc.)

15 b) defect coordinates in wafers and spatial clusters of defect coordinates in the case of spatial cluster analysis, and

c) other information pertaining to the defect that may be have been developed a priori, including but not limited to image type information such as in list a) and b), compositional or electrical information derived from analytic techniques and information pertaining to the processing history, yield relevance or origins of the defects  
20 in question. It will be understood that any appropriate features can be extracted without departing from the spirit of the present invention. Examples of analytical techniques used to derive compositional or electrical information are described in "Semiconductor Characterization: Present Status & Future Needs" ed. W.M. Bullis, D.G. Seiler, A.C. Diebold, American Institute of Physics 1996, ISBN 1-56396-503-8 which contains an  
25 overview of the myriad ways of analyzing defects and their yield relevance and which is herein incorporated by reference.

Supervised Automatic Dynamic Classifier software 204 uses the extracted features of the images in the training set to classify the images in the working set (W) that were not selected by the user as part of the training set (T) (i.e., to classify the set of  
30 images W-T). In a preferred embodiment of the invention, the classifier 204 uses a

nearest neighbor method in which the features are extracted from the image set W-T and each image in W-T is classified as belonging to a class. In general, an image in W-T belongs to the class whose members have feature vectors most closely resembling the feature vector of the image. It should be understood that other automatic classification methods could be used. For example, the features in the feature vectors could be weighted (either by the user or using a predefined weighting).

Once the images in the set W-T is classified by classifier 204, the results of the automatic classification are compared with the results of the user's manual classification. If the user has classified any images that are also classified by classifier 204, the results are compared and the comparison displayed in a visual form to the user. The user may, at this point, alter his classification scheme and make alterations to the training set of images accordingly. The user may also change his manual classification if he decides that the automatic classification looks more correct.

Fig. 3 shows an interface generated by defect classifier software in accordance with a preferred embodiment of the present invention. Specifically, Fig. 3 shows an example of a "Smart Gallery" window in accordance with a preferred embodiment of the invention. One of the main purposes behind the Smart Gallery window is to provide a gallery-based classification capability. The Smart Gallery allows the user to view thumbnail images of the defects at adjustable sizes and resolutions, presenting a group of defects in an organized fashion. It also assists the user in the classification scheme creation by providing defects in groups based on appearance.

The benefits of the Smart Gallery system include:

- Defect sorting
- Allowing the human user to perform quicker and more efficient classification and organization of his manual sorting schemes
- Faster manual sorting scheme creation
- Shorter manual classification time
- Better manual classification quality (repeatability)
- Quicker, more effective classifier creation process

10

15

20

25

30

the classes/bins. Once the training set is indicated, the automatic classifier classifies the remaining images. The classifier 204 can also use the training set to classify some other set of images. In different embodiments, classifier 204 can either run in the background, reclassifying images whenever the training set is changed, or it can be run explicitly by the user. The result of the classifier 204 is compared to any manual classification done by the user and the comparison results are displayed on the confusion matrix. The user can then add or subtract images to or from the training set via 310 of Fig. 3 in accordance with the contents of the confusion matrix.

The images can be automatically grouped according to invariant core classes, such as those shown in PCT Publication No. WO 00/03234 (inventor: Ben-Porath et al.), published January 20, 2000, which is herein incorporated by reference. The results of the ADC process could also be incorporated in an overall fab yield management system, such as that shown in PCT Publication No. WO 99/59200 (inventor: Lamey et al.), published November 18, 1999, which is herein incorporated by reference. This application also incorporates by reference PCT Publication No. WO 99/67626 (inventor: Ravid) published December 29, 1999.

Fig. 4(a) shows an example of confusion matrix 306 where the manual and automatic classification are in agreement (see diagonal elements 402). In the example, one image is agreed to be in class “3”, three images are agreed to be in class “2” and one image is agreed to be in class “1”. Clicking on a “Correct” button next to the matrix will cause results in agreement to be highlighted. Clicking on a “known errors” button will cause results not in agreement to be highlighted. Clicking on the “image” button allows the user to view the images that were used to generate a particular element in the matrix.

Fig. 4(b) shows an example of the confusion matrix 306 where the manual and automatic classification are not in agreement. Element 452 is a non-zero element off the diagonal of the matrix.

Figs. 5(a) and 5(b) shows respective examples of an interface 502, 504 that allows the user to display in sorted order the images in the working set and in the training set. The images are preferably sorted by such factors as lot number, manual bin, suggested bin, and size.

Certain embodiments use a Spatial Signature Analysis (SSA) Technique, as described in 1) [http:// www-ismv.ic.ornl.gov/projects/SSA.html](http://www-ismv.ic.ornl.gov/projects/SSA.html); 2) Gleason S.S., Tobin K.W., & Karnowski, T.P., "Spatial Signature Analysis of Semiconductor Defects for Manufacturing Problem Diagnosis", *Solid State Technology*, July, 1996; 3) <http://www.dym.com/ssa.htm>; 4) [http://www.electroglas.com/products/knights\\_datasheets/spar\\_ds.htm](http://www.electroglas.com/products/knights_datasheets/spar_ds.htm); and 5) [http://www.ornl.gov/Press\\_Releases/archive/mr19980804-00.html](http://www.ornl.gov/Press_Releases/archive/mr19980804-00.html), which are herein incorporated by reference.

In addition, in certain embodiments, the analysis and classification are not limited to images, but can be performed on clusters themselves. In such an embodiment, the classifier receives “cluster-based features” instead of raw images as described in T.P. Karnowski, K.W. Tobin, S.S. Gleason, Fred Lakhani, SPIE's 24th Annual International Symposium on Metrology, Inspection and Process Control for Microlithography XIII, Santa Clara Convention Center, Santa Clara, CA, February, 1999, which is herein incorporated by reference. Such a system applies grouping and Kohonen mapping to clusters instead of to raw images. For non-image data, clustering is gathered by EDS specifiers (using an x-ray system for analysis in an e-beam system) or by SSA analysis.

Fig. 7 shows an expanded view of training area 312. The user can drag images from the working group 308 into the displayed groups in area 310 or into the classes/bins of area 312. In the displayed embodiment, each class/bin has a class code, a

group code (reflecting its natural group) and a number of defect images currently assigned to the class/group. The user can, of course, add and delete classes/bins as he wishes (e.g., via the toolbar).

If the user wants to add a new class/bin, the class/bin is added. Other wise, an existing class/bin is opened. The user then manually adds images/defects to the class. Features are extracted from the selected images and stored during a “train classifier” operation (e.g., via the toolbar). The classifier then classifies a set of images (such as the set W-T) and the user reviews the errors found in the classifier’s decisions. For example, the user may view the confusion matrix to determine where the classifier differed form the user’s classifications. The user then improves the training set by adding deleting, or reclassifying images via, e.g., a drag and drop interface and reassesses the classifier’s performance until a satisfactory result is achieved.

Fig. 8 shows an example user interface that includes a “Smart Gallery” setup function 802, an Auto Classifier Creation function 804, and a Classifier function 806.

The Smart Gallery setup function leads to the user interface of Fig. 3. The classifier function leads to the user interface of Fig. 9. “Smart Gallery is a trademark of KLA-Tencor Corporation.

Fig. 9 shows an additional user interface for an embodiment of the Automatic Classifier Function. This embodiment is an alternative to the toolbar-driven, drag and drop method. Using this interface, a user can add defect images to the training set 902 and specify the features to extract for natural grouping and for the feature extractor of the classifier 904. The user can specify the number of features to extract (here, 80). When the user selects a Train button 906, the features of the images in the training set are extracted and saved as feature vectors for each image. The class/bin of each image is saved in association with the feature vector.

The user can set filters 908 on the images, removing certain groups, images, and types of images from the features extraction process. The user can also adjust the confidence of the feature method methods used by the classifier 204 using button 910.

When the user clicks Test (Training set) button 912, the classifier 204 classifies the set of images W-T into the bins in the training set in accordance with the feature vectors of the images in the training set.

Figs. 10(a) and 10(b) are block diagrams of systems in accordance with the present invention distributed over a network, such as the internet or an intranet. In Fig. 10(a), an optical, ebeam, or other types of inspection systems 1002, a classifier 1004/104, and an analysis system 1006 (see Fig. 1) are distributed over the network. In Fig. 10(b), elements of classifier 1004, including a natural grouping process 1054, an automatic supervised classifier 1056/204, and a feature extractor 1058, are distributed over the network. Natural grouping process 1054 receives as inputs the features of the working set and outputs the natural groupings of the working set. Automatic supervised classifier 1056 receives the features and classes of the training set and the features of the defect images, while outputting the classes of the defect images being classified. Feature extractor 1058 receives images and outputs features of the images.

Fig. 10(b) also shows an embodiment in which the classifier receives tool history 1005 as an input. Tool history includes, for example, the maintenance history of the tools or machine performing the inspection process and/or the manufacturing process. If the tool has been maintained according to its suggested maintenance schedule, its data may be weighted more than data from an unmaintained tool. Tool History 1055 may also include a threshold of inspection value, indicating that maintenance must be found in order for the classifier to give credence to the data from that tool. This threshold may vary for individual tools or may be the same for all the tools of a particular type or function. Tool history may also indicate, for example, whether two runs of semiconductors were taken from the same tool (or which tool they were taken from). Thus, tool history 1055 may include, for example, equipment Ids. If it is known, for example, that Tool A has had problems in the past, data from tool A may be treated differently than data from a trouble-free tool B.

As described above, Fig. 10(c) shows that the inspection and analysis/classification process is performed in real-time during the inspection process instead of as a separate process. In such a system, inspection system 1052 is shown



inside the system 1004 to indicate that it is part of the same system as the classifier 1056. The system utilizes review images ("patch" images) to effect real-time inspection. New inspection systems such as the KLA 2350 include ADC embedded inside the image computer running in 'real time' during the inspection. This system, called an iADC 5 (integrated ADC) system, works by grabbing 'patches' around the defect location during scan and performing ADC on the defective pixels in these patches. All this is done in hardware inside the inspector so that no addition throughput is required.

10 Figs. 10(a), 10(b), and 10(c) each contain a dotted line 1003, 1053 depicting that, in certain systems, the classifier can provide feedback signals to the inspection system, in a similar manner discussed above in connection with Fig. 1.

15 It will be understood that various embodiments and alternations can exist without departing from the spirit and scope of the invention. For example, the concept of the invention can be extended to include automatically sorting images in the background (for example while running defect analysis software) by defect type, and then displaying the result as a wafer map with a distribution of each selected type shown over the wafer 20 map. This is just one way of using the output data. Defect location distribution can be helpful in identifying defect source, so the ability to select similar defects (natural grouping) coupled with the ability to see their spatial distribution could be powerful. A display can be included showing for each cluster in the Kohonen map a) a representative image, and b) a defect map showing the locations of the defects in the cluster.

25 From the above description, it will be apparent that the invention disclosed herein provides a novel and advantageous system and method of optical inspection used to classify semiconductor defects.

What is Claimed is:

1. A method implemented by a data processing system for classifying a plurality of received images, comprising:

5                extracting features from a training set that is a user-chosen subset of the plurality of images, each image in the training set having an associated class;  
                 classifying, by the data processing system, at least one of the plurality of images in accordance with the extracted features and classes of the training set;  
                 allowing a user to classify ones of the plurality of images; and  
10               displaying the results of a comparison between the classification by the data processing system and the classification by the user.

2. The method of claim 1, wherein the features of the training set include size.

15               3. The method of claim 1, wherein the features of the training set include brightness.

4. The method of claim 1, wherein the features of the training set include color.

20               5. The method of claim 1, wherein the features of the training set include shape.

6. The method of claim 1, wherein the features of the training set consist at least one of: texture, moment of inertia, context, proximity to wafer features, proximity to other defects, connectivity to adjacent features, connectivity to other defects, and yield  
25               relevant properties derived from the image.

7. The method of claim 1, wherein the features of the training set include defect coordinates in wafers.

8. The method of claim 1, wherein the features of the training set include defect coordinates when spatial cluster analysis is used.

5 9. The method of claim 1, wherein the features of the training set include information derived from one of the processing history, yield, relevance, and origins of defects.

10 10. The method of claim 1, where classifying, by the data processing system, at least one of the plurality of images in accordance with the extracted features and classes of the training set includes classifying the plurality of images using a Kohonen map technique.

15 11. The method of claim 10, wherein the Kohonen map is seeded with non-random numbers.

20 12. The method of claim 1, where classifying, by the data processing system, at least one of the plurality of images in accordance with the extracted features and classes of the training set includes classifying the plurality of images using a spatial signature analysis technique.

13. The method of claim 1, where classifying, by the data processing system, at least one of the plurality of images further includes classifying in accordance with cluster-based features instead of images.

25 14. The method of claim 1, wherein allowing a user to classify ones of the plurality of images includes displaying the images to the user in classification groups determined by the classifying step.

30 15. The method of claim 1, further comprising sending feedback to an inspection system to fine-tune the inspection system in accordance with the user's classification.



a software portion configured to allow a user to classify ones of the plurality of images; and

a software portion configured to send feedback to an inspection system to fine-tune the inspection system in accordance with the user's classification.

5

22. The system of claim 21, wherein the features of the training set include size.

23. The system of claim 21, wherein the features of the training set include brightness.

10

24. The system of claim 21, wherein the features of the training set include color.

25. The system of claim 21, wherein the features of the training set include shape.

15

26. The system of claim 21, wherein the features of the training set consist at least one of: texture, moment of inertia, context, proximity to wafer features, proximity to other defects, connectivity to adjacent features, connectivity to other defects, and yield relevant properties derived from the image.

20

27. The system of claim 21, wherein the features of the training set include defect coordinates in wafers.

28. The system of claim 21, wherein the features of the training set include defect coordinates when spatial cluster analysis is used.

25

29. The system of claim 21, wherein the features of the training set include information derived from one of the processing history, yield, relevance, and origins of defects.

30



37. The system of claim 21, wherein the features include tool history information relating to an inspection system.

38. The system of claim 21, wherein the features include tool history information relating to the past success rate of the classification step.

39. The system of claim 21, wherein only some of the plurality of images relate to a semiconductor etch process.

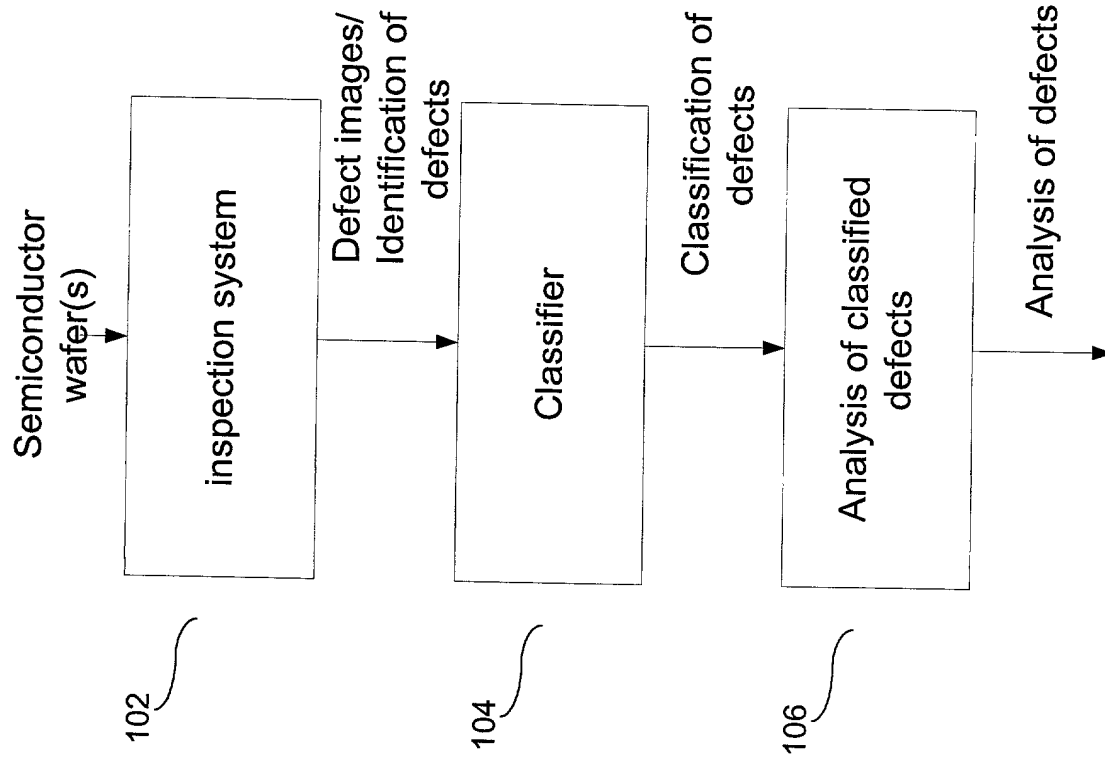
40. A system that classifies a plurality of received images, comprising:  
a portion configured to extract features from a training set that is a user-chosen subset of the plurality of images, each image in the training set having an associated class;  
a portion configured to classify, by the system, at least one of the plurality of images in accordance with the extracted features and classes of the training set;  
a portion configured to send feedback to an inspection system to fine tune the inspection system in accordance with the classification performed by the data processing system.

## **Power Assisted Automatic Supervised Classifier Creation Tool for Semiconductor Defects**

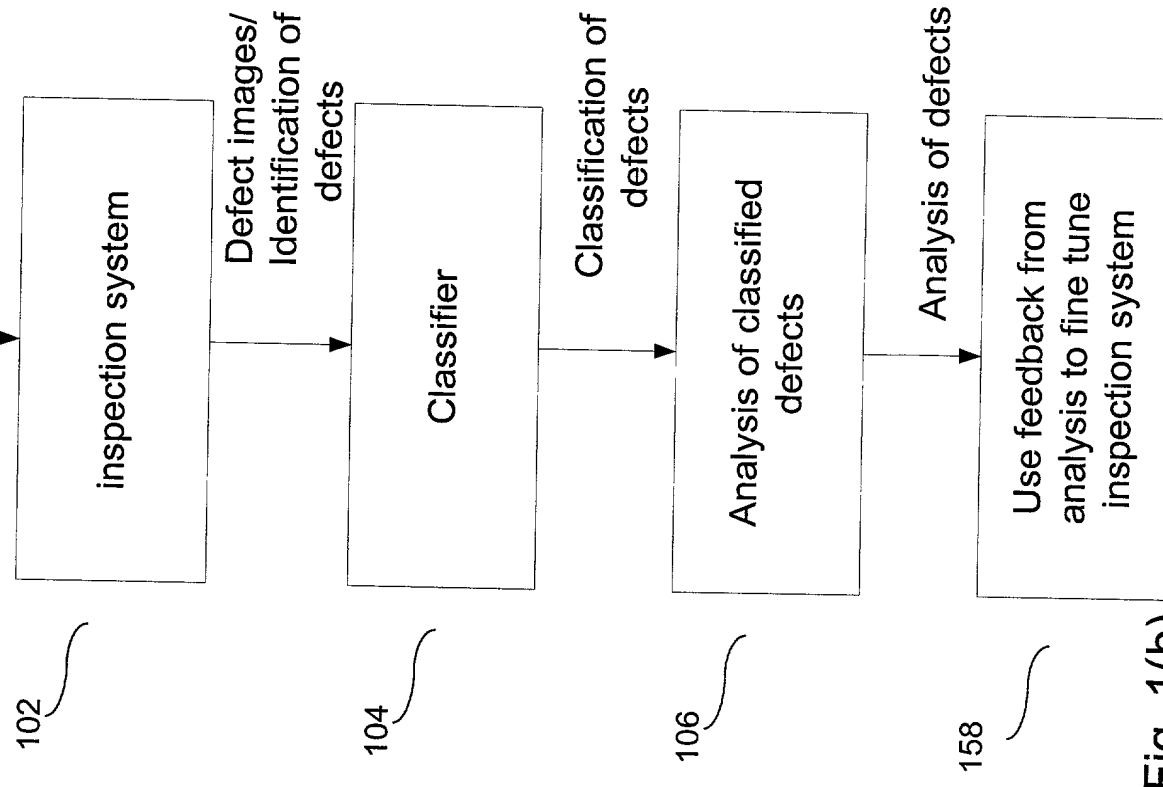
### **ABSTRACT OF THE DISCLOSURE**

A method and system that optionally allows a user to view image defects organized by natural groupings based on features of the images. The natural groupings make it easier for the user to organize some or all of the images into classes in a training set of images. A feature vector is extracted from each image in the training set and stored, along with its user-specified class, for use by an automatic classifier software module. The automatic classifier uses the stored feature vectors and classes to automatically classify images not in the training set. If the automatically classified images do not match images manually classified by the user, the user modifies the training set until a better result is obtained from the automatic classifier. The system can provide feedback to an inspection system designed to aid in the setup and fine-tuning of the inspection system.





**Fig. 1(a)**



**Fig. 1(b)**



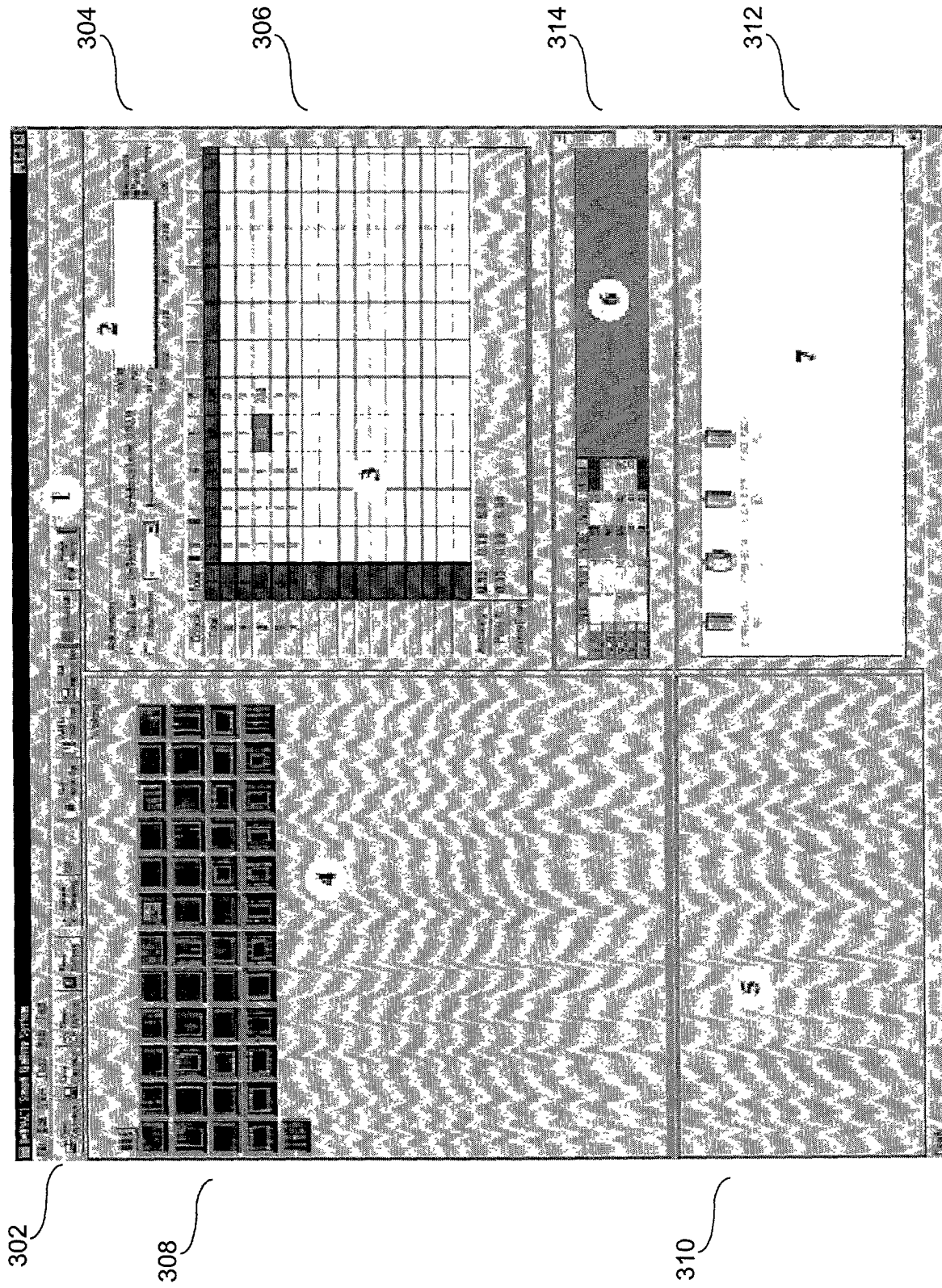


Fig. 3  
Smart Gallery window



502

**Sort Properties**

Working Set | Training Set

**Primary Key**

☒ Lot - Wafer  
☐ Manual Bin  
☐ Suggested Bin  
☐ Size

**Secondary Key**

☐ Lot - Wafer  
☐ Manual Bin  
☐ Suggested Bin  
☐ Size

OK Reset Cancel

Fig. 5(a)

Sort Properties

Working Set

Training Set

Primary Key

Lot - Wafer

Manual Bin

Suggested Bin

Size

Secondary Key

Lot - Wafer

Manual Bin

Suggested Bin

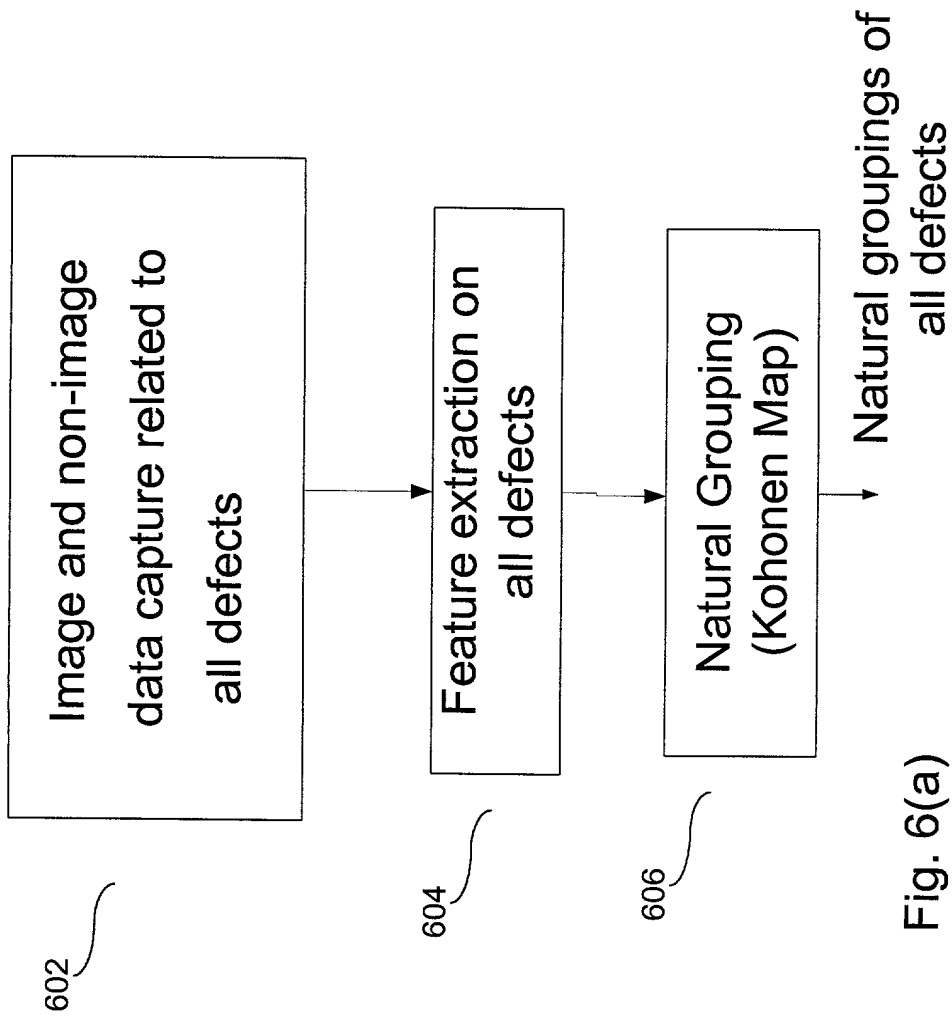
Size

OK

Reset

Cancel

Fig. 5(b)





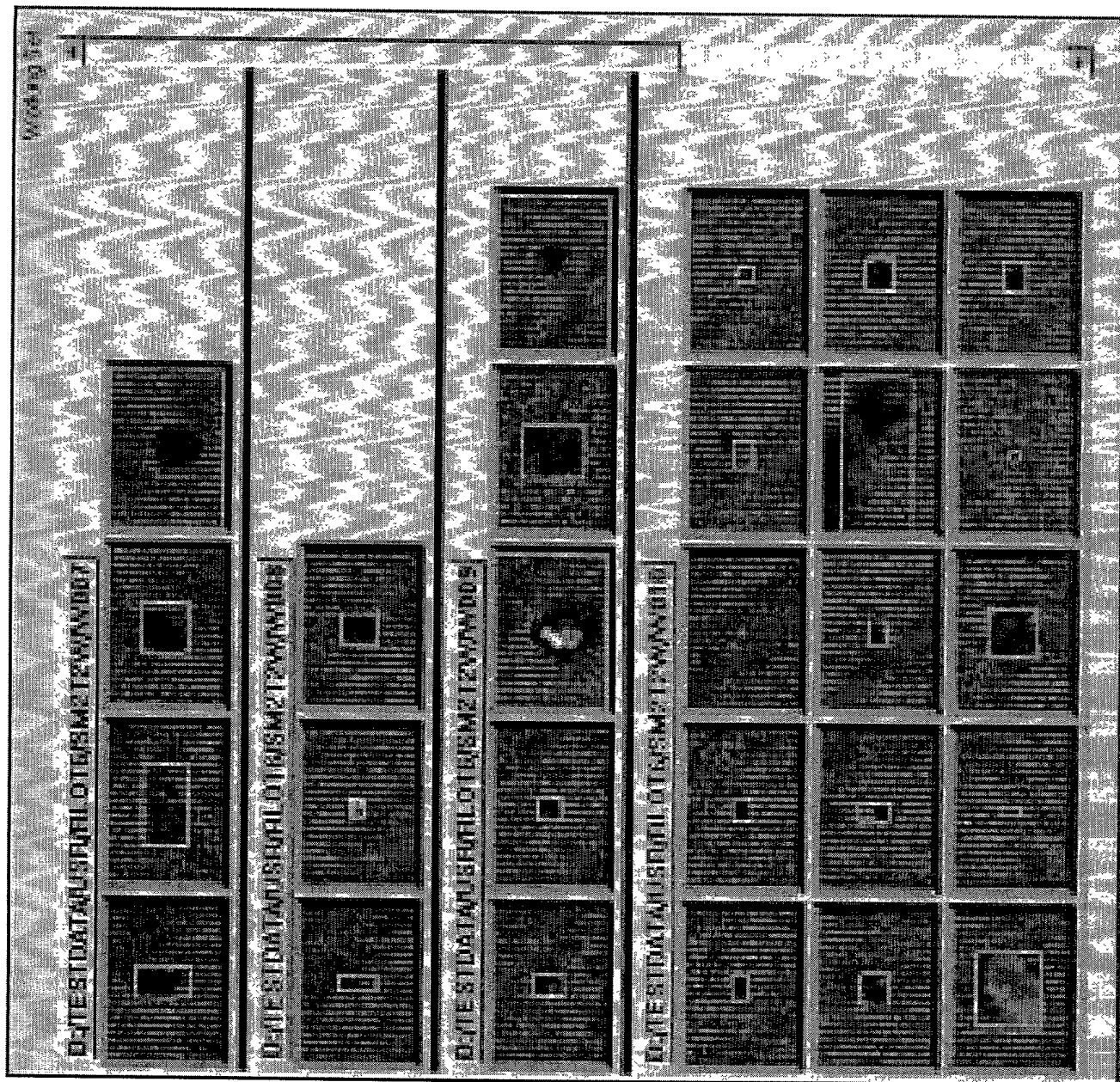


Fig. 6(b)



312

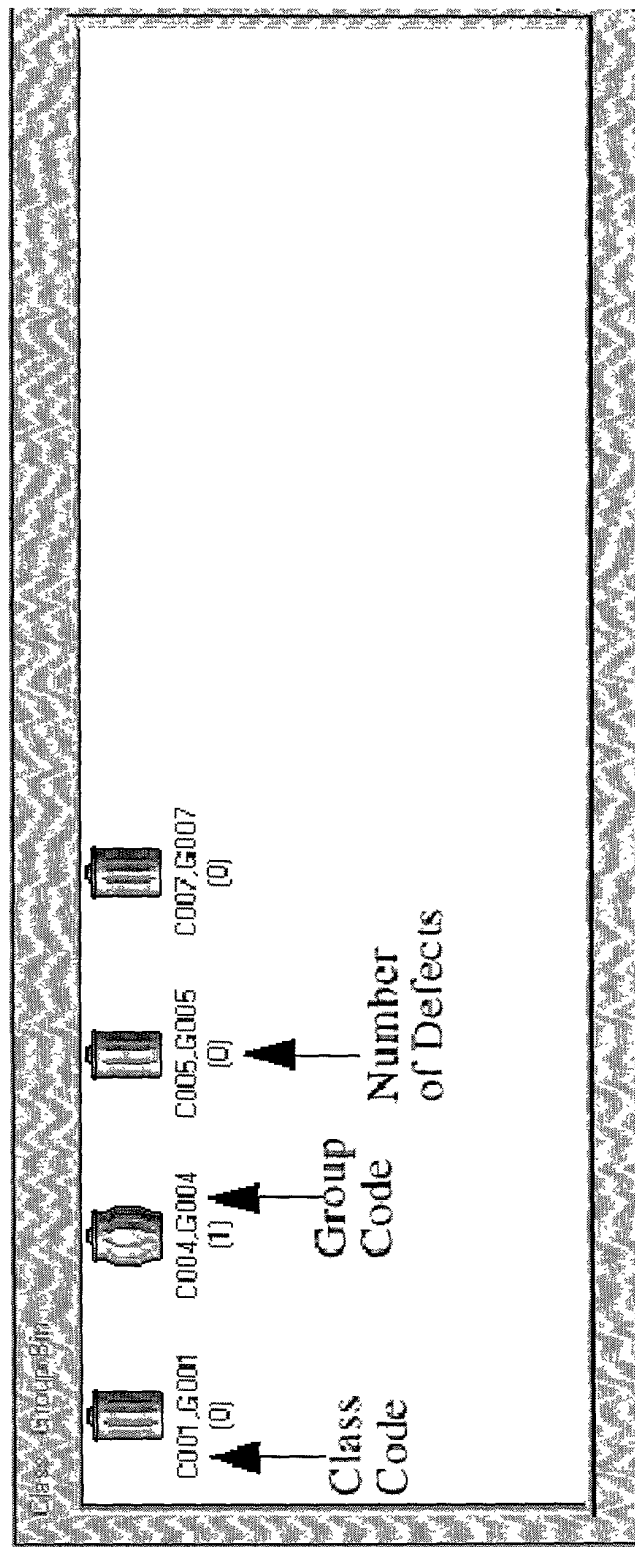
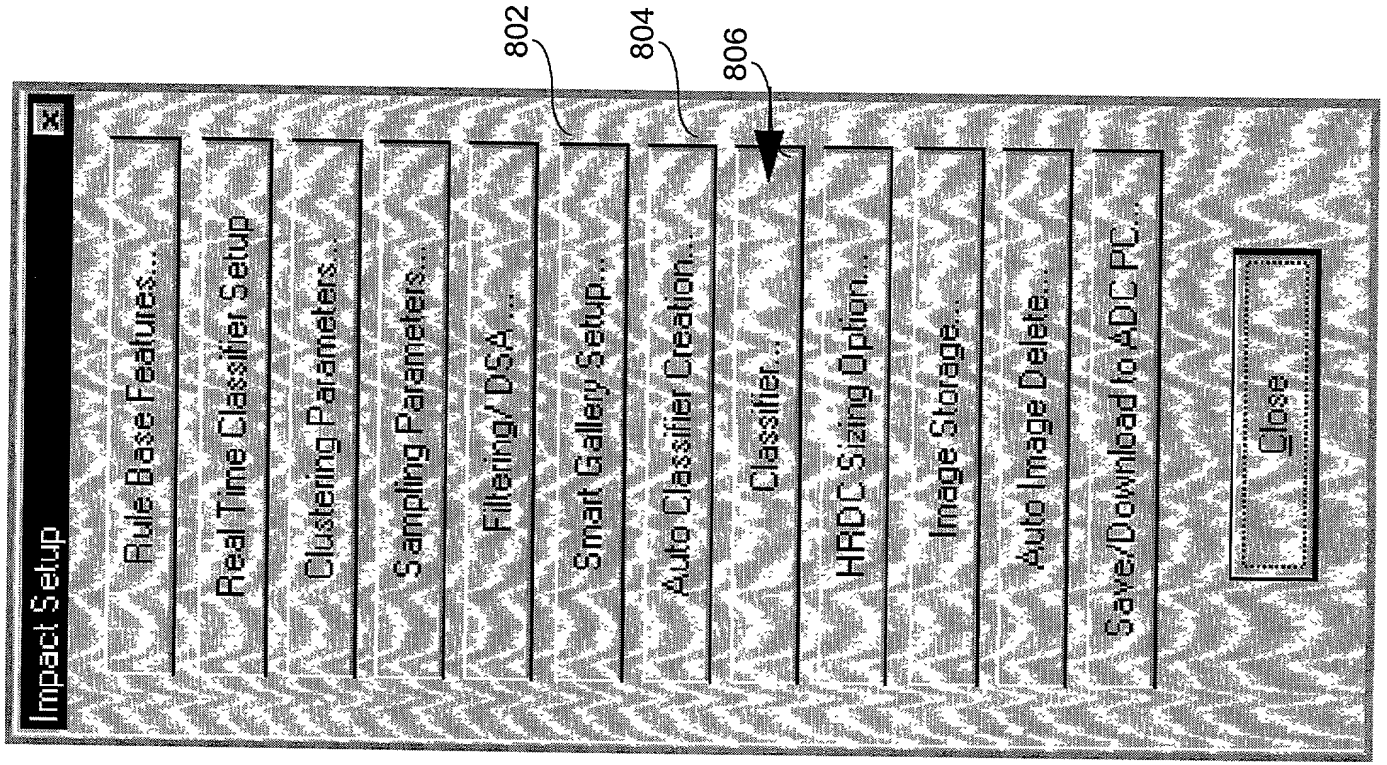


Fig. 7

Fig. 8



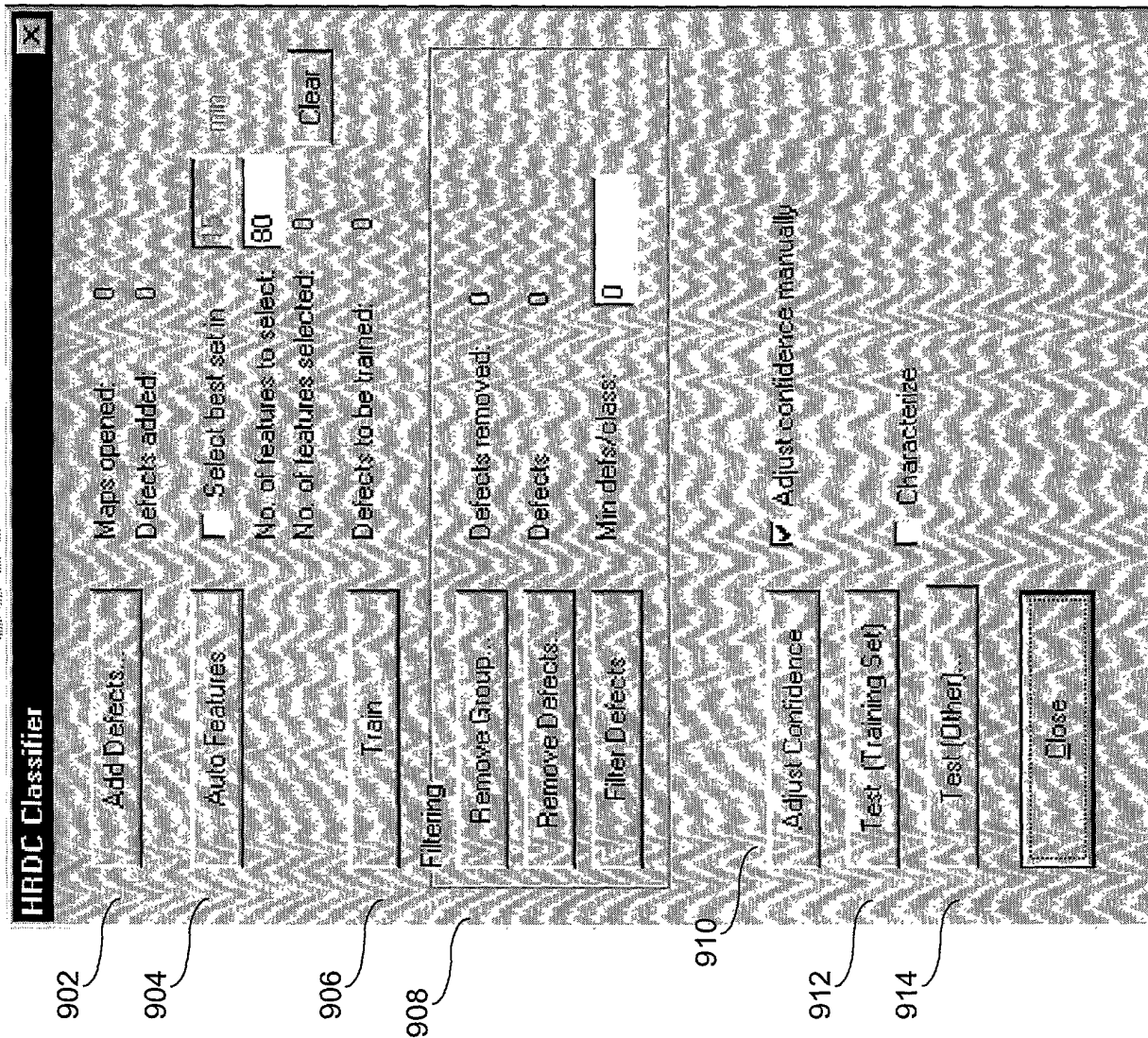


Fig. 9

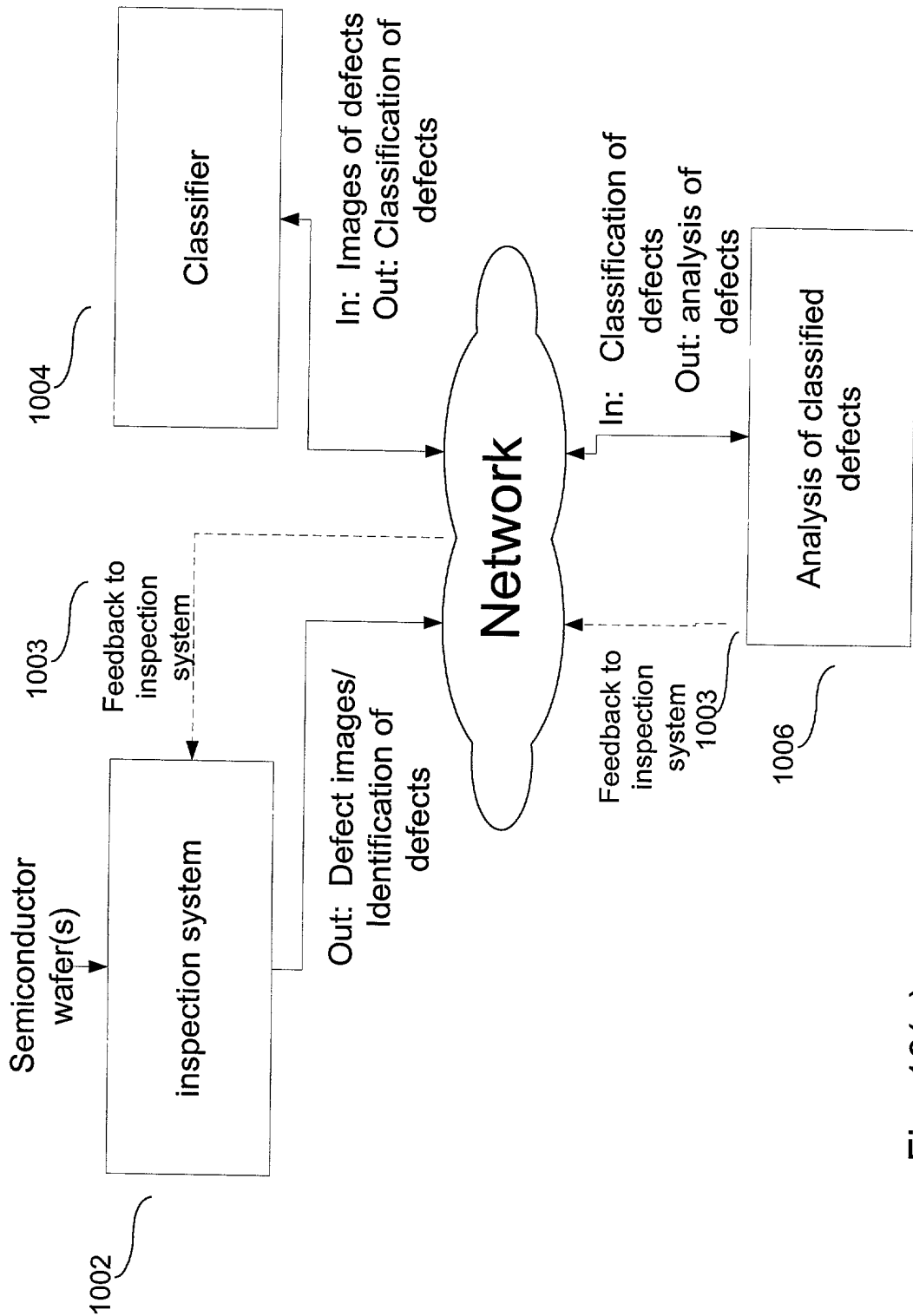


Fig. 10(a)

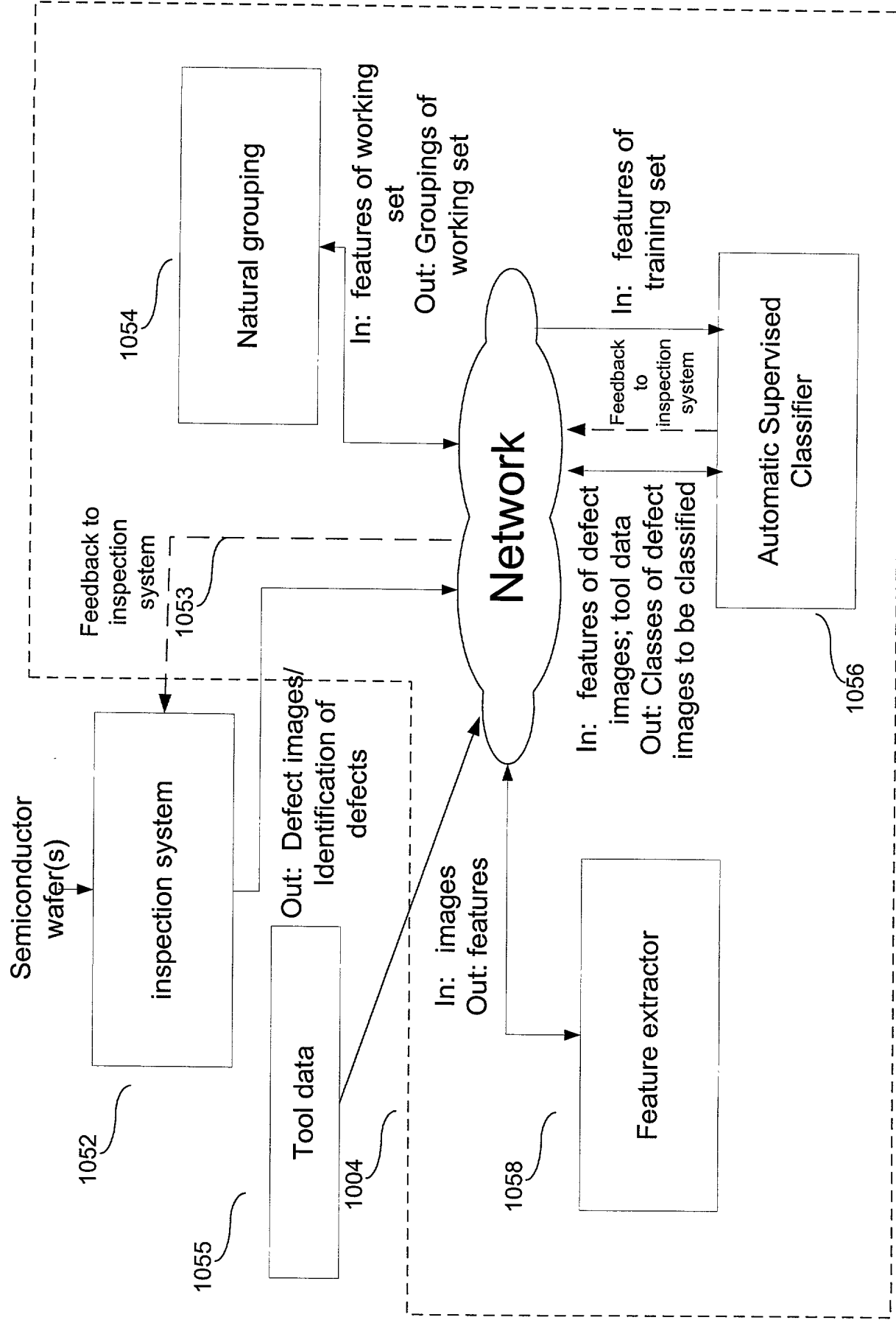


Fig. 10(b)

Semiconductor  
wafer(s)

1004

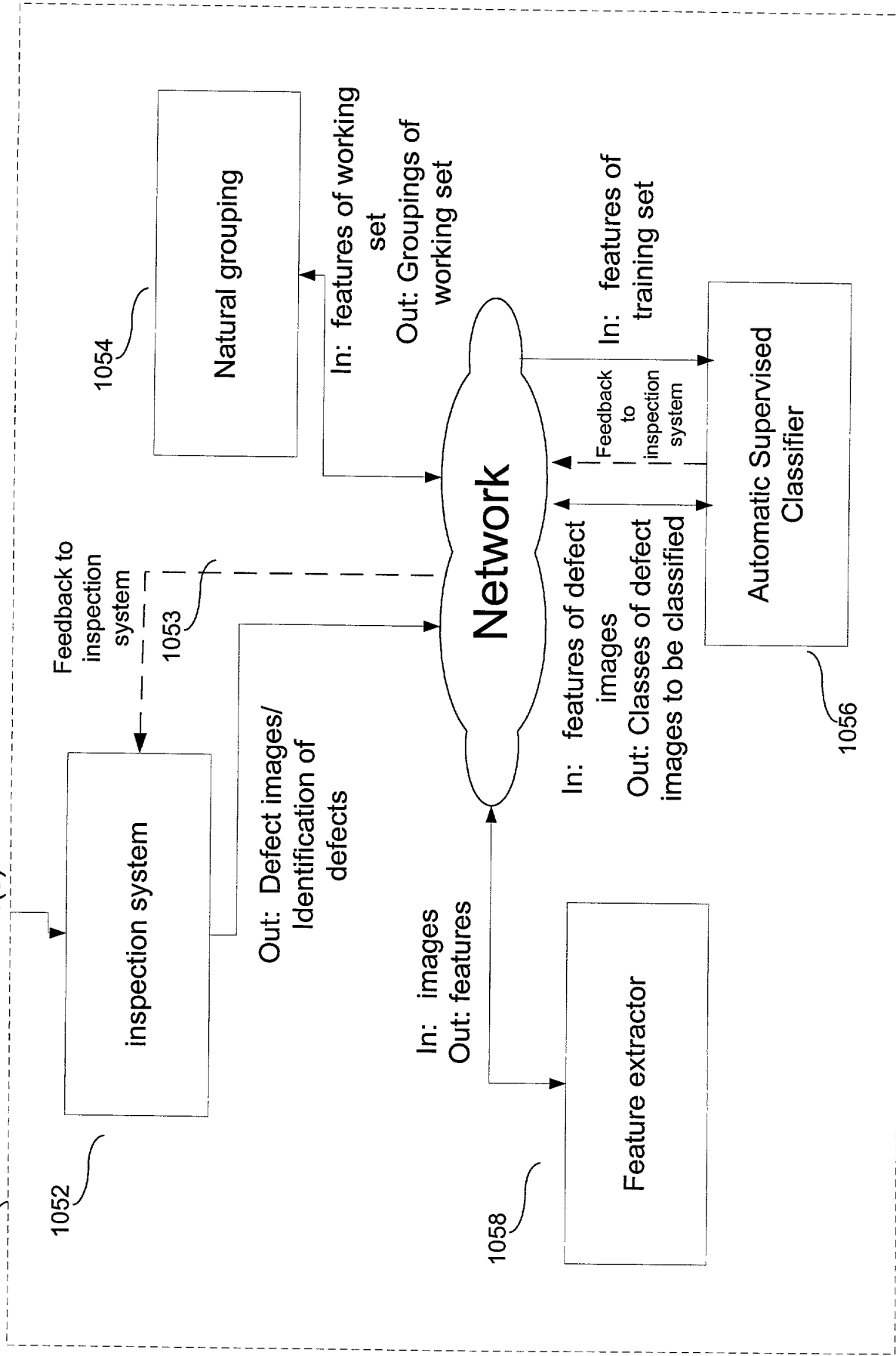


Fig. 10(c)

<b>0010/PTO</b> Rev. 6/95  <b>U.S. Department of Commerce</b> Patent and Trademark Office  <b>DECLARATION FOR UTILITY OR DESIGN PATENT APPLICATION</b>  <input checked="" type="checkbox"/> Declaration Submitted with Initial Filing      OR <input type="checkbox"/> Declaration Submitted after Initial Filing	Attorney Docket Number	<b>4616 US</b>
	First Named Inventor	<b>Bakker</b>
	<i>COMPLETE IF KNOWN</i>	
	Application Number	<b>To Be Assigned</b>
	Filing Date	<b>Herewith</b>
	Group Art Unit	<b>To Be Assigned</b>
	Examiner Name	<b>To Be Assigned</b>

As a below named inventor, I hereby declare that:

My residence, mailing address, and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

**Power Assisted Automatic Supervised Classifier Creation Tool For Semiconductor Defects**

the specification of which

*(Title of the Invention)*

☒ is attached hereto

OR

☐ was filed on (MM/DD/YYYY) [ ] as United States Application Number or PCT International Application Number [ ] and was amended on (MM/DD/YYYY) [ ] (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment specifically referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37 Code of Federal Regulations. § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code § 119 (a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365 (a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application Number(s)	Country	Foreign Filing Date (MM/DD/YYYY)	Priority Not Claimed	Certified Copy Attached?	
				YES	NO
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

☐ Additional foreign application numbers are listed on a supplemental priority sheet attached hereto:

I hereby claim the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below.

Application Number(s)	Filing Date (MM/DD/YYYY)	<input type="checkbox"/> Additional provisional application numbers are listed on a supplemental sheet attached hereto.
<b>60/167,955</b>	<b>11/29/1999</b>	

**DECLARATION**

Page 2

I hereby claim the benefit under Title 35, United States Code § 120 of any United States application(s), or § 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of Title 35, United States Code § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

U.S. Parent Application Number	PCT Parent Number	Parent Filing Date (MM/DD/YYYY)	Parent Patent Number (if applicable)

☐ Additional U.S. or PCT international application numbers are listed on a supplemental priority sheet attached hereto.

As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

Name	Registration Number	Name	Registration Number
<b>Greg T. Sueoka</b>	<b>33,800</b>		
<b>Trinidad Arriola Kern</b>	<b>44,012</b>		
<b>Laura A. Majerus</b>	<b>33,417</b>		
<b>Charles E. Schulman</b>	<b>43,350</b>		

☐ Additional attorney(s) and/or agent(s) named on a supplemental sheet attached hereto.

Please direct all correspondence to:

**Laura A. Majerus**  
**Fenwick & West LLP**  
**Two Palo Alto Square**  
**Palo Alto, CA 94306**  
**U.S.A.**

Telephone (650) 858-7152 Fax (650) 494-1417

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

**Name of Sole or First Inventor:**

☐ A petition has been filed for this unsigned inventor

Given Name	<b>David</b>	Middle Initial		Family Name	<b>Bakker</b>	Suffix e.g. Jr.		
Inventor's Signature					Date			
Residence: City			State		Country		Citizenship	
Mailing Address								
Mailing Address								
City			State		Zip		Country	



DECLARATION				ADDITIONAL INVENTOR(S) Supplemental Sheet			
<b>Name of Additional Joint Inventor, if any:</b>				<input type="checkbox"/> A petition has been filed for this unsigned inventor			
Given Name	Saibal	Middle Initial		Family Name	Banerjee	Suffix e.g. Jr.	
Inventor's Signature					Date		
Residence: City		State		Country		Citizenship	
Mailing Address							
Mailing Address							
City	State		Zip	Country			

<b>Name of Additional Joint Inventor, if any:</b>				<input type="checkbox"/> A petition has been filed for this unsigned inventor			
Given Name	Ian	Middle Initial		Family Name	Smith	Suffix e.g. Jr.	
Inventor's Signature					Date		
Residence: City		State		Country		Citizenship	
Mailing Address							
Mailing Address							
City	State		Zip	Country			

<b>Name of Additional Joint Inventor, if any:</b>				<input type="checkbox"/> A petition has been filed for this unsigned inventor			
Given Name		Middle Initial		Family Name		Suffix e.g. Jr.	
Inventor's Signature					Date		
Residence: City		State		Country		Citizenship	
Mailing Address							
Mailing Address							
City	State		Zip	Country			

<b>Name of Additional Joint Inventor, if any:</b>				<input type="checkbox"/> A petition has been filed for this unsigned inventor			
Given Name		Middle Initial		Family Name		Suffix e.g. Jr.	
Inventor's Signature					Date		
Residence: City		State		Country		Citizenship	
Mailing Address							
Mailing Address							
City	State		Zip	Country			
<input type="checkbox"/> Additional inventors are being named on supplemental sheet(s) attached hereto							